Title:

Integrated, ultrafast all-optical polariton transistors

Abstract:

Since the breakdown of Dennard scaling approximately 15 years ago, the clock frequency of processors has remained stalled at a few GHz. Although all-optical transistors that can switch at THz speed could bring a leap in performance, this promise was not fulfilled during decades of research due to low optical nonlinearities and bulky components. Now the foundations of a new generation of devices are investigated that harnesses the so-called strong light-matter interaction regime with novel materials and integrated photonic structures that could enable compact, ultrafast all-optical logic circuits with attojoule switching energy [1,2].

In this work, the experimental progress towards this goal will be presented, including a cascading setup where a spontaneous polariton condensate is created in one cavity (Seed) and fed into another cavity (Transistor) to induce polariton condensation [3,4]. Additionally, rapid polariton condensation dynamics on a sub-picosecond timescale will be presented, and important transistor metrics such as signal amplification (up to a factor of 60) and on/off extinction ratio (up to 9:1) will be determined.

These findings indicate the potential for developing integrated, ultrafast all-optical transistors that are scalable, allowing for more complex all-optical logic circuits.

Furthermore, a method for controlling these ultrafast all-optical transistors will be presented, utilising phase-change material-based memory cells.

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References

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